Camelback Computer Architecture, LLC
Consulting Services

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OBJECTIVE	Technical consulting position for planning, implementing, validating, and optimizing microprocessor-based products. Open to full- or part-time; locations in Utah and California preferred.
SKILLS	**Product Planning** • Working with customers, engineers, product marketing to define product features and implementation plan • Familiar with industry hardware and software standards (e.g., PCI-E, HT, USB, ACPI) **Performance Evaluation** • Microbenchmarks • Standard system benchmarks (SPECcpu, SPECsfs, TPC) • Optimize code using V-Tune and other performance monitoring tools **Validation** • Focused and random test generation • PC compatibility validation • Pre- and post-silicon **Programming** • High-level languages (C, FORTRAN) • Assembly language (e.g., x86, ARM, MIPS) **Communication** • Writing technical documentation • Presentations for engineering/business review and decisions • Lectures for instruction
SAMPLE PROJECTS	**1989-2002** Led technical definition of Pentium^{TM} Processor and 815 chipset Led microarchitecture definition and validation of Itanium^{TM} Processor **2004** Developed proof-of-concept demonstration for communication protocol on ARM development board using PC-based development tools **2005-2006** Evaluated VLSI design of proprietary search algorithm for networking chips Presented cost/performance/competitive analysis to executive team for business decision **2005-2007** Refined microarchitecture definition for low-power PC processor Documented for internal training and patent applications